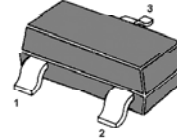


PNP Silicon Epitaxial Planar Transistor



1. Base 2. Emitter 3. Collector
SOT-23 Plastic Package

Absolute Maximum Ratings ($T_a = 25\text{ }^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Collector Base Voltage	$-V_{CBO}$	40	V
Collector Emitter Voltage	$-V_{CEO}$	40	V
Emitter Base Voltage	$-V_{EBO}$	5	V
Collector Current	$-I_C$	1	A
Peak Pulse Current	$-I_{CM}$	2	A
Power Dissipation	P_{tot}	200	mW
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_S	- 65 to + 150	$^\circ\text{C}$

Characteristics at $T_a = 25\text{ }^\circ\text{C}$

Parameter	Symbol	Min.	Max.	Unit
DC Current Gain				
at $-V_{CE} = 5\text{ V}$, $-I_C = 1\text{ mA}$	h_{FE}	300	-	-
at $-V_{CE} = 5\text{ V}$, $-I_C = 100\text{ mA}$	h_{FE}	300	800	-
at $-V_{CE} = 5\text{ V}$, $-I_C = 500\text{ mA}$	h_{FE}	250	-	-
at $-V_{CE} = 5\text{ V}$, $-I_C = 1\text{ A}$	h_{FE}	160	-	-
Collector Cutoff Current				
at $-V_{CB} = 30\text{ V}$	$-I_{CBO}$	-	0.1	μA
Collector Cutoff Current				
at $-V_{CE} = 30\text{ V}$	$-I_{CEO}$	-	0.1	μA
Emitter Cutoff Current				
at $-V_{EB} = 5\text{ V}$	$-I_{EBO}$	-	0.1	μA
Collector Emitter Saturation Voltage				
at $-I_C = 100\text{ mA}$, $-I_B = 1\text{ mA}$	$-V_{CEsat}$	-	0.2	V
at $-I_C = 500\text{ mA}$, $-I_B = 20\text{ mA}$		-	0.35	
at $-I_C = 1\text{ A}$, $-I_B = 100\text{ mA}$		-	0.5	
Base Emitter Saturation Voltage				
at $-I_C = 1\text{ A}$, $-I_B = 50\text{ mA}$	$-V_{BEsat}$	-	1.1	V
Base Emitter Voltage				
at $-I_C = 1\text{ A}$, $-V_{CE} = 5\text{ V}$	$-V_{BE}$	-	1	V
Collector Capacitance				
at $-V_{CB} = 10\text{ V}$, $f = 1\text{ MHz}$	C_C	-	12	pF
Gain Bandwidth Product				
at $-V_{CE} = 10\text{ V}$, $-I_C = 50\text{ mA}$, $f = 100\text{ MHz}$	f_T	150	-	MHz



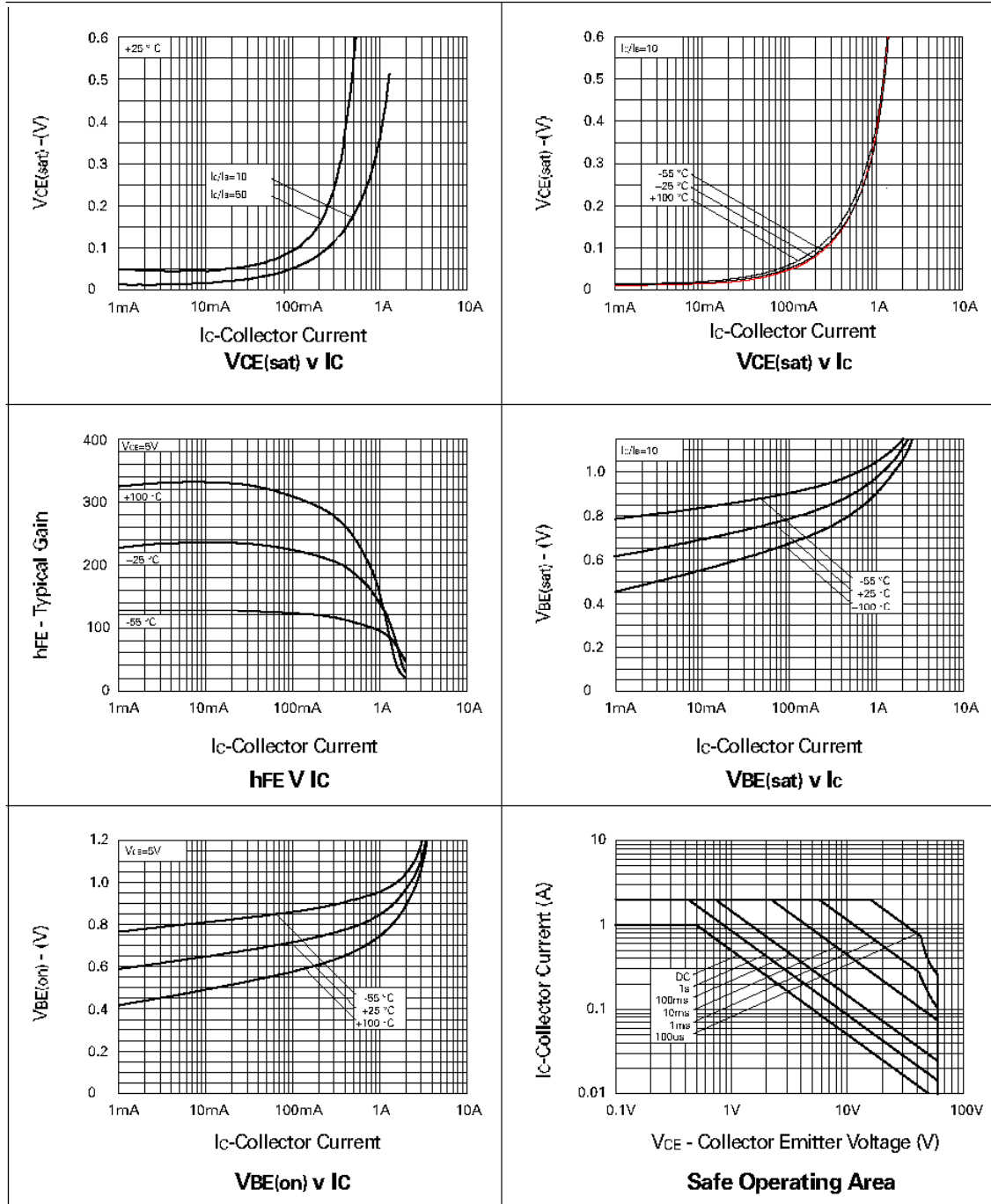
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SOT-23

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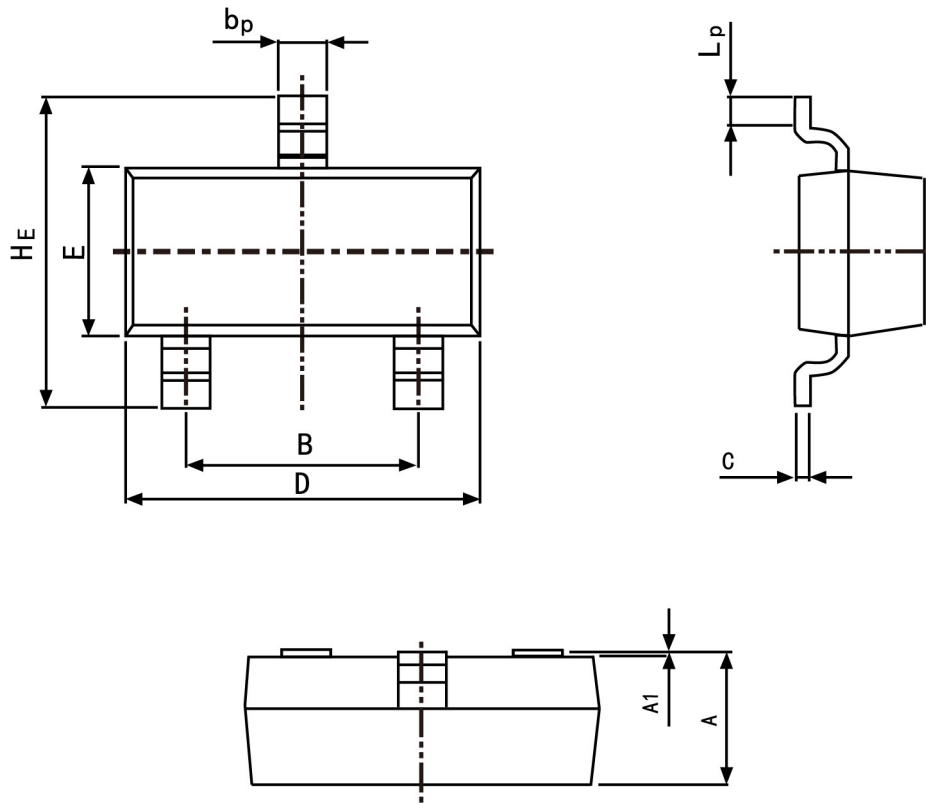




PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



Symbol	Dimension in Millimeters	
	Min	Max
A	0.95	1.40
B	1.78	2.04
b_p	0.35	0.50
C	0.08	0.19
D	2.70	3.10
E	1.20	1.65
HE	2.20	3.00
A1	0.100	0.013
L_p	0.20	0.50